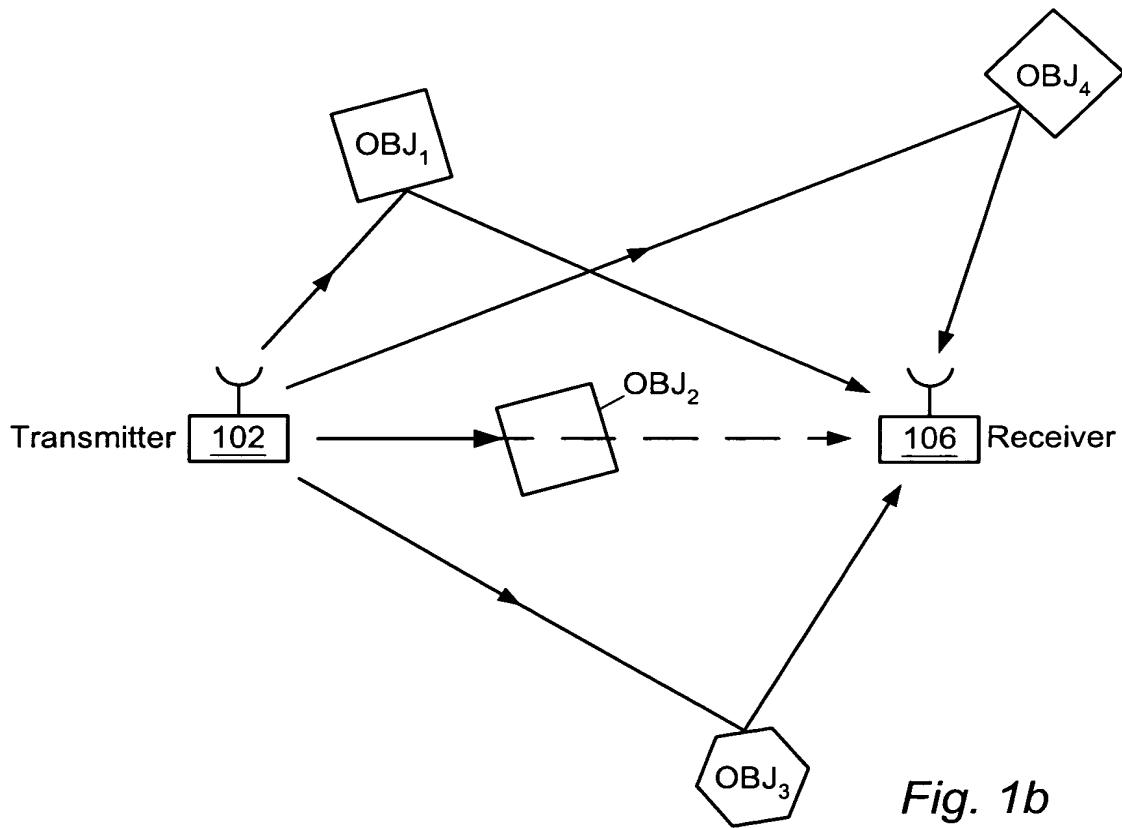
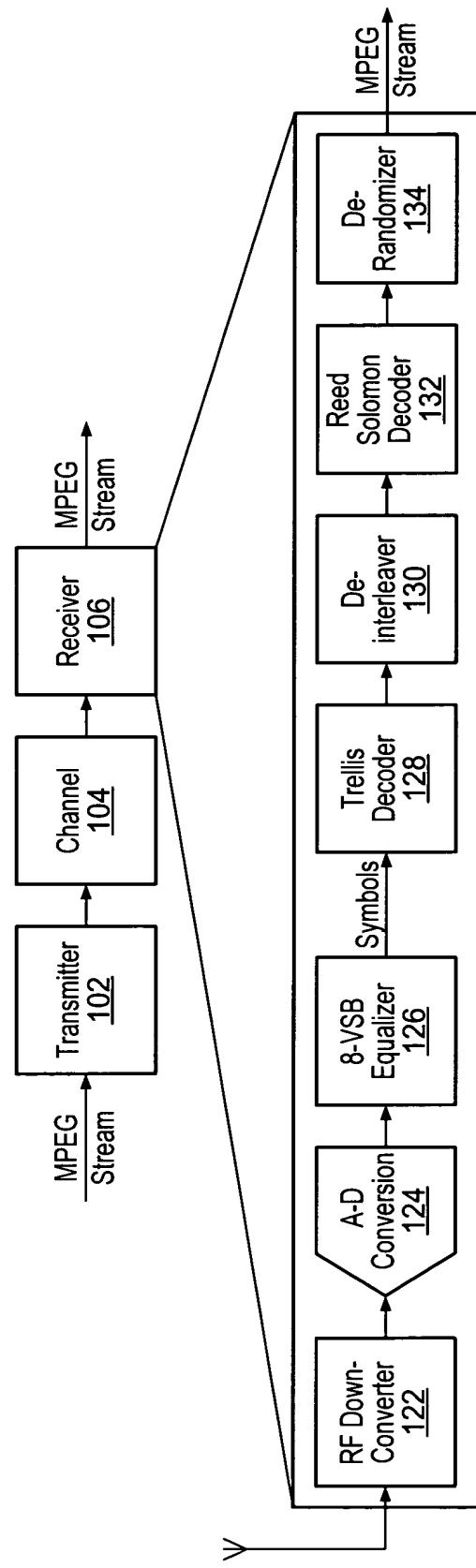


*Fig. 1a*



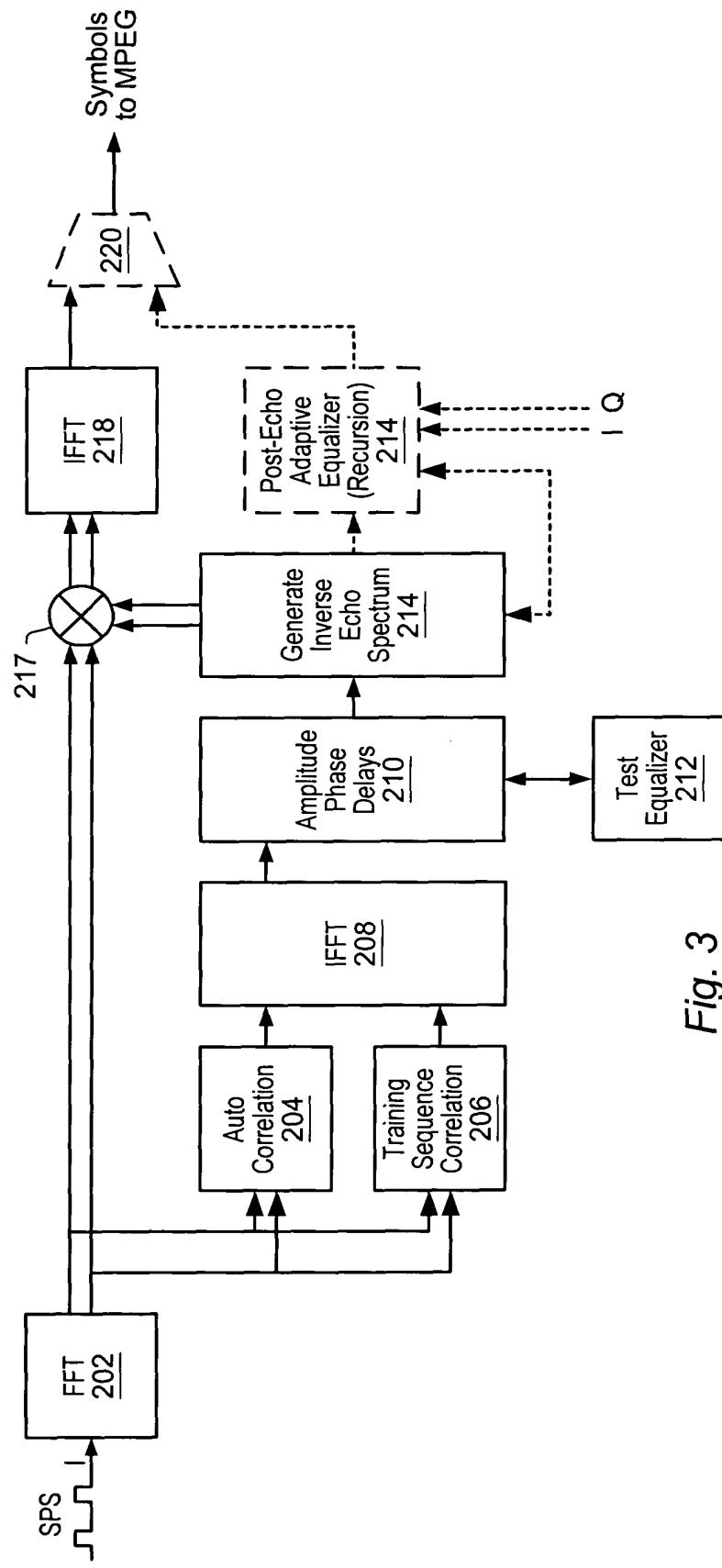
*Fig. 1b*

**Block diagram of HDTV channel and receiver signal processing chain.**



*Fig. 2*

**Overview of frequency Domain Equalization Algorithm**



*Fig. 3*

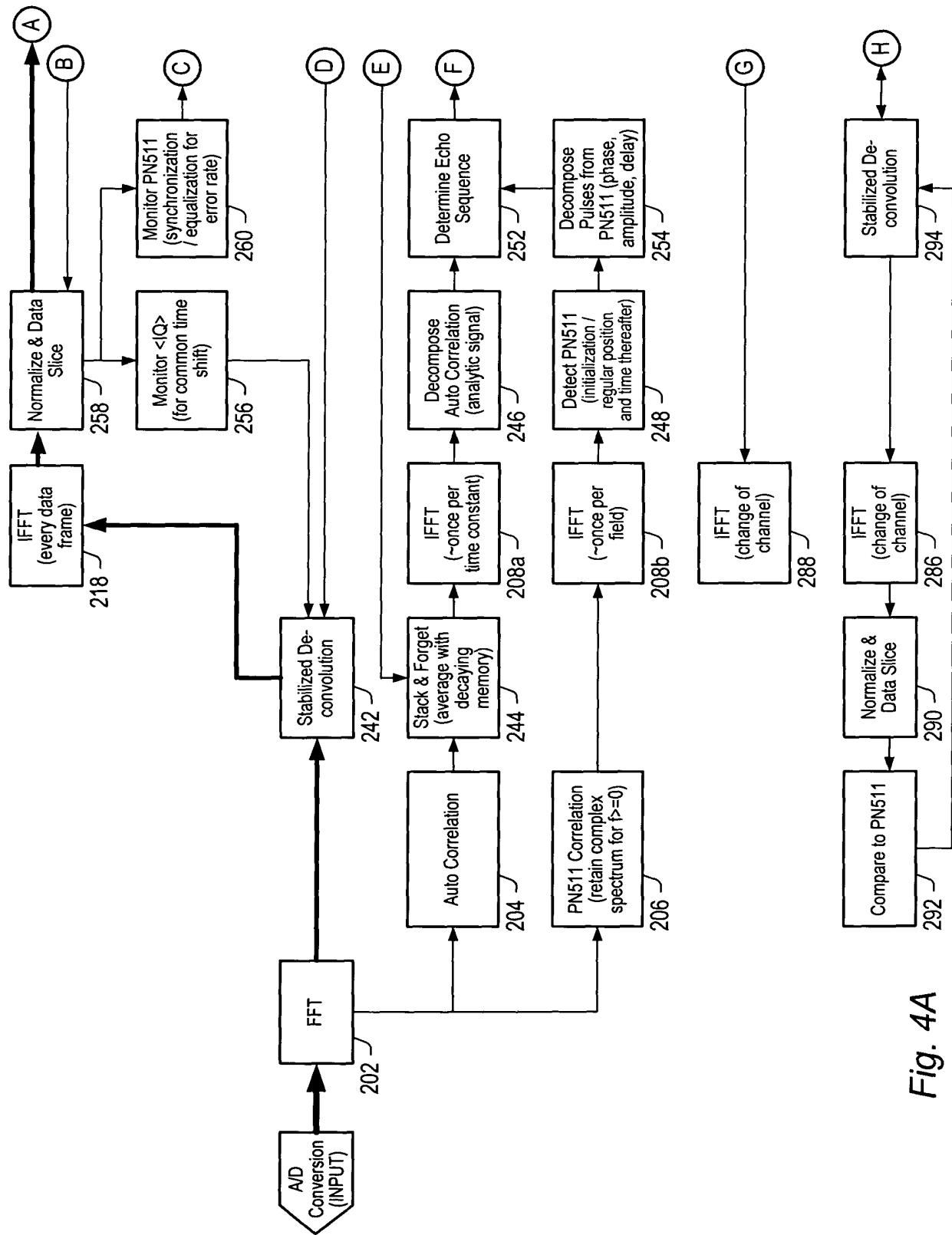


Fig. 4A

*Fig. 4B*

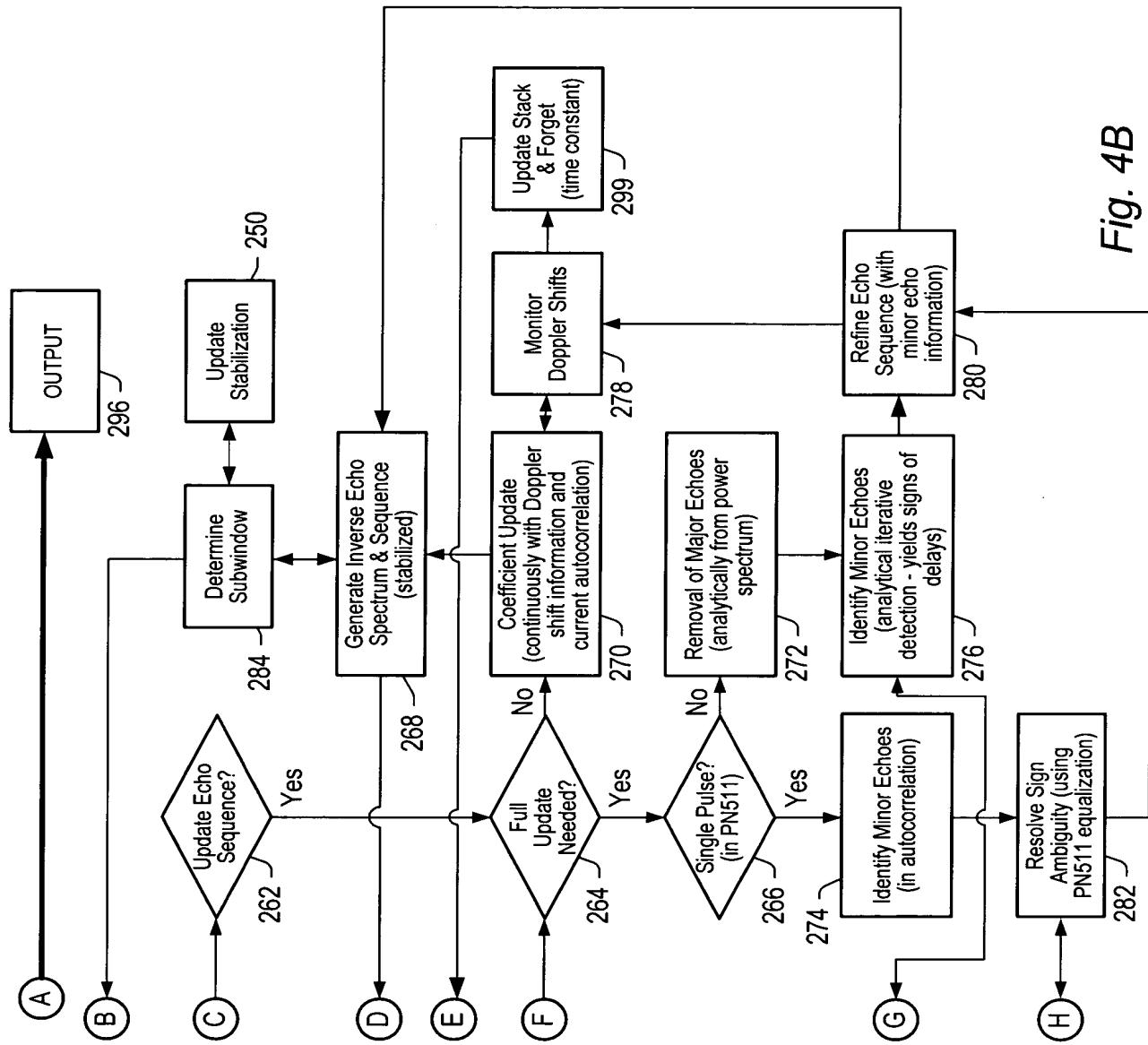
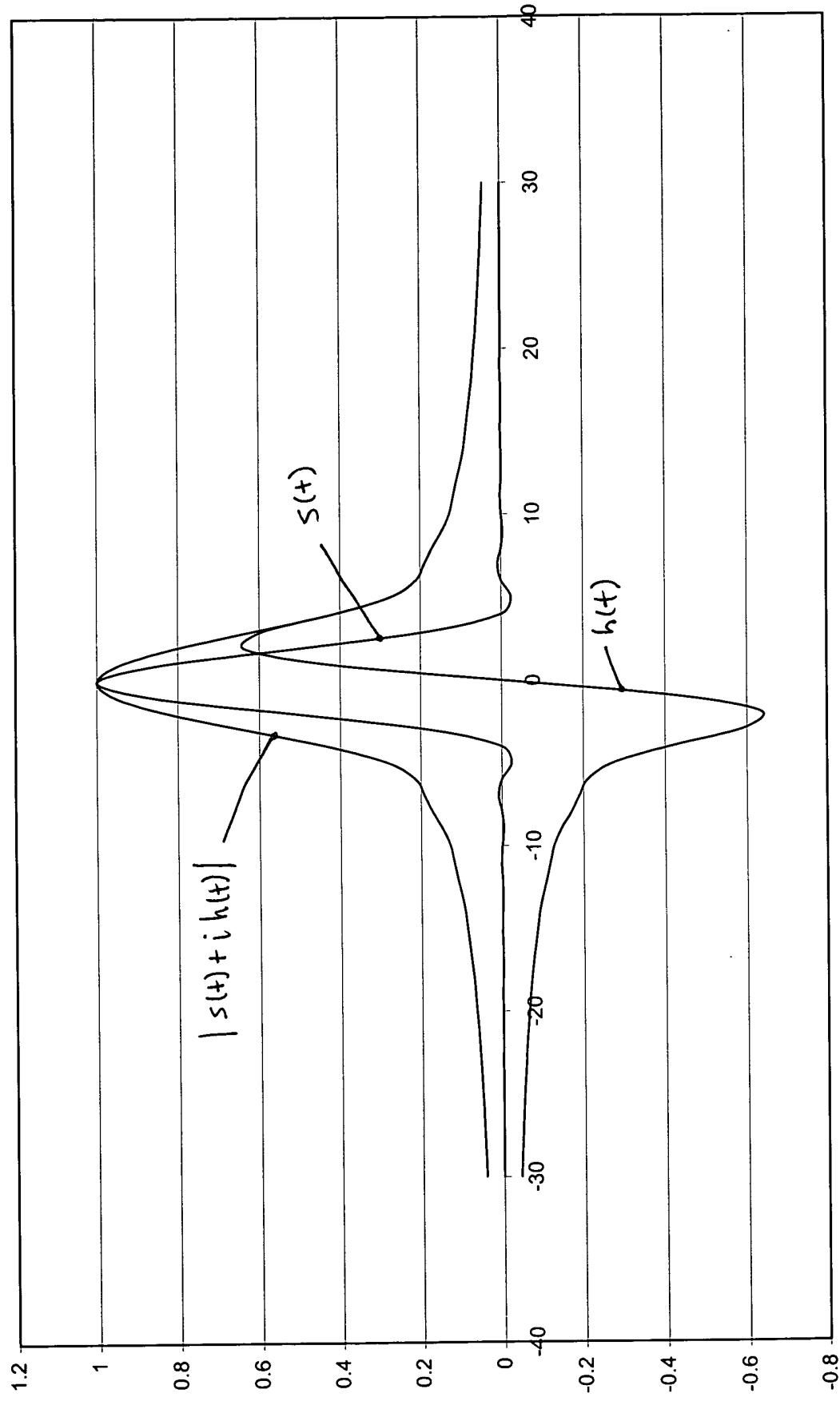


Fig. 5



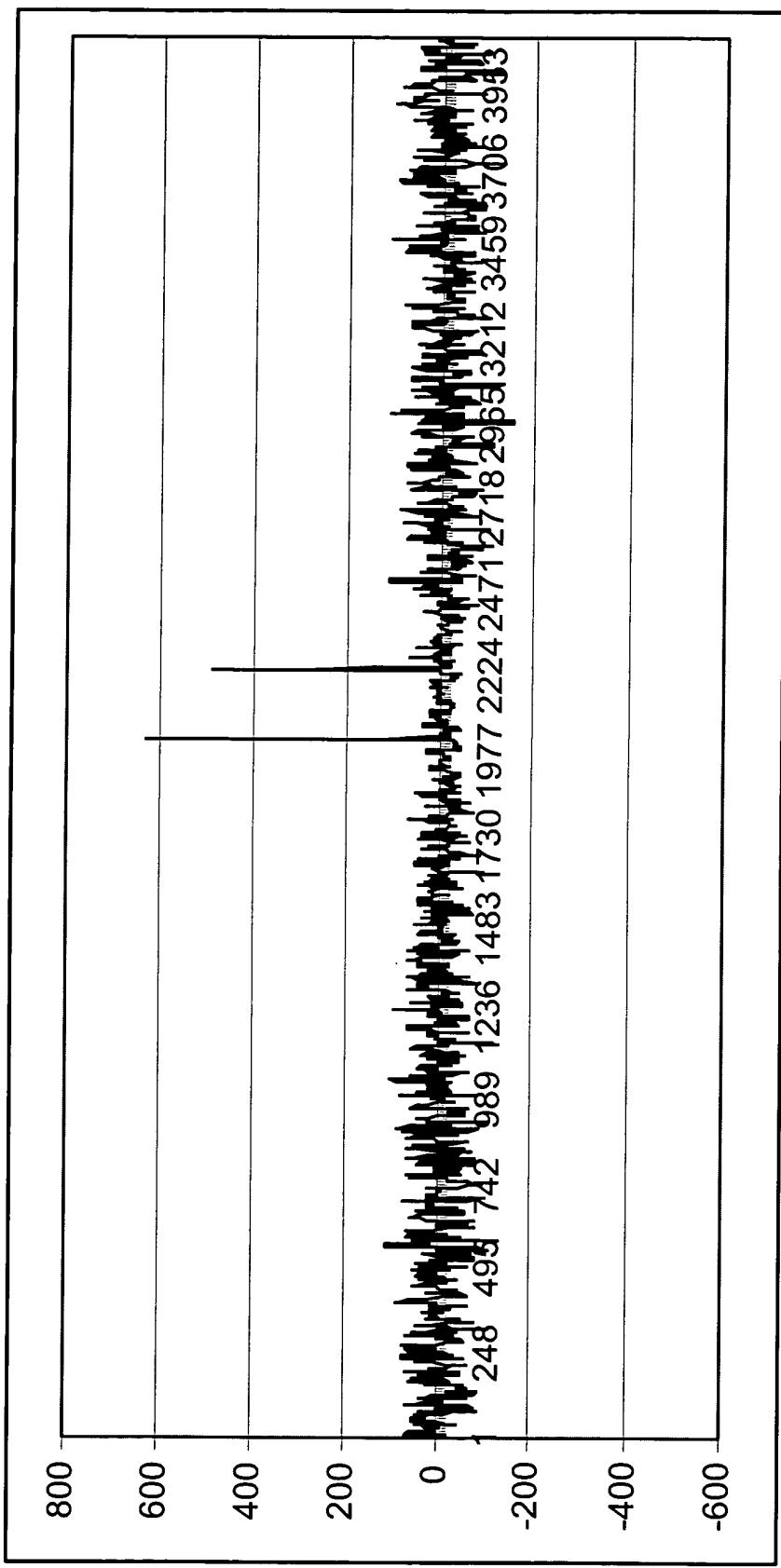


Fig. 6A

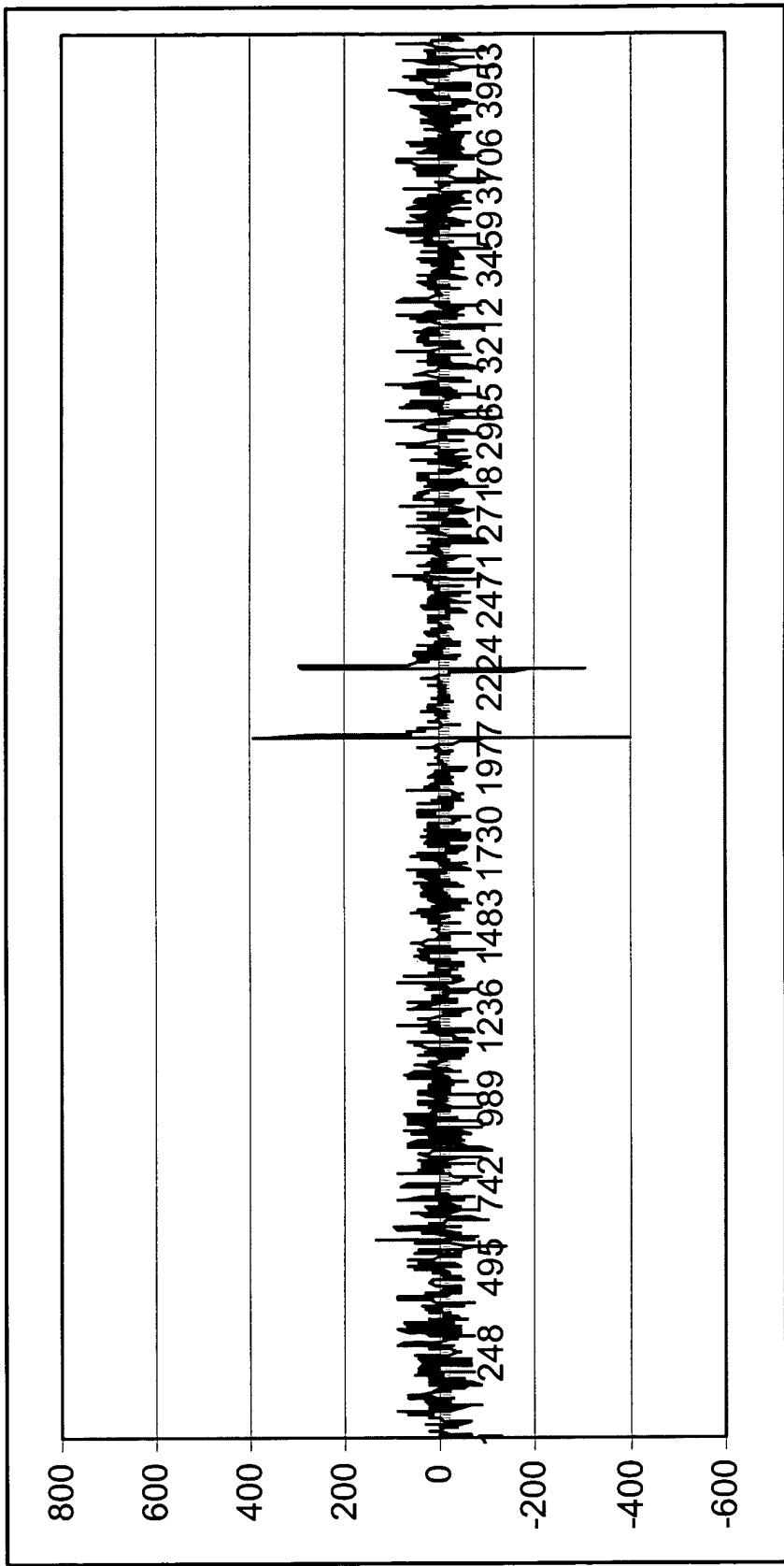
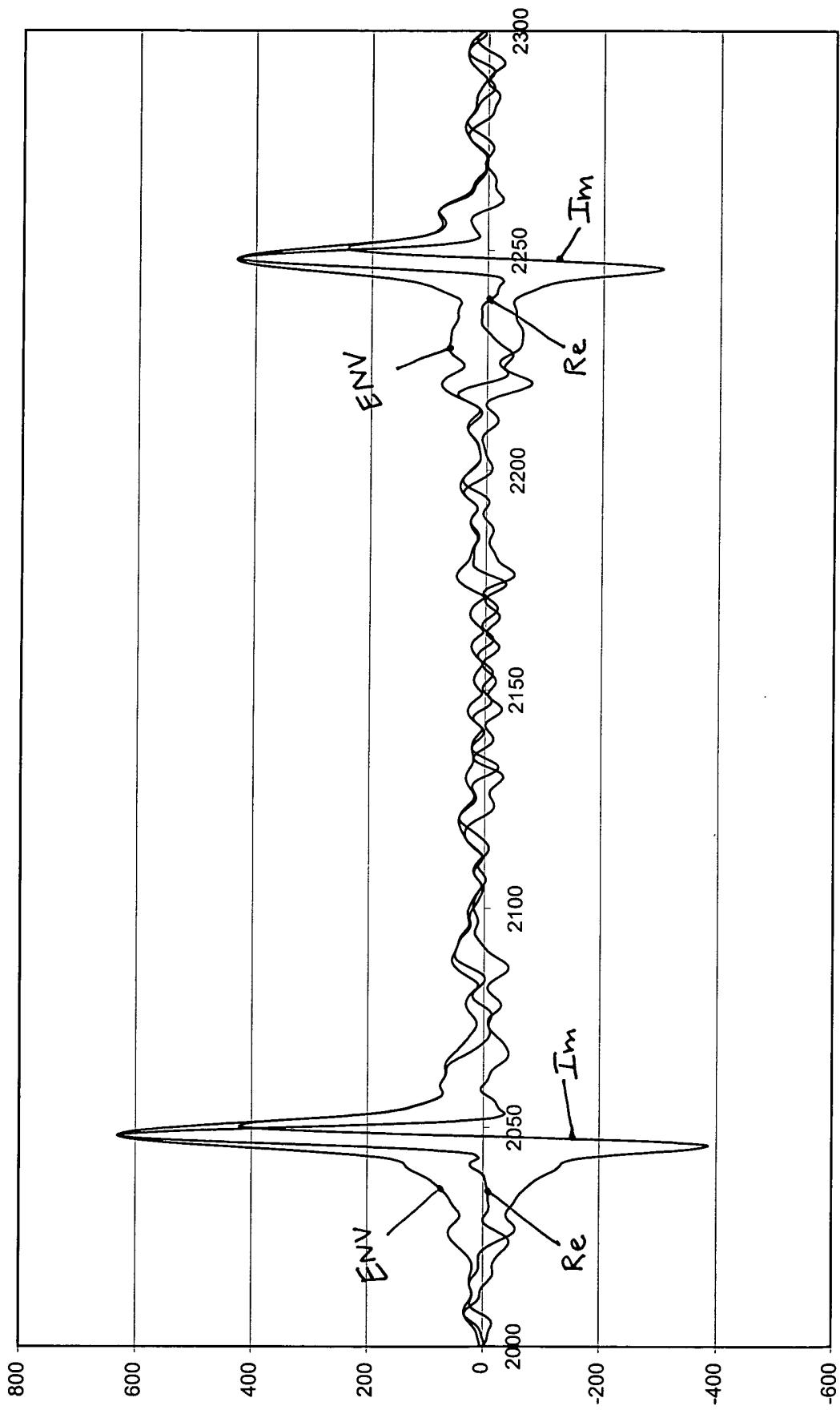


Fig. 6B

**Fig. 7**



**Fig. 08**

